



Nvis 6561 Shift Registers Trainer is a compact, ready to use digital electronics experiment board. This training product has been designed specifically for students to study all the four types of 3-bit and 4-bit shift registers on a single board and verify their truth tables.

A shift register is capable of shifting its binary information either to the left or TO THE right direction. The temporary memory storage and data

shifting characteristics of shift registers make them extremely valuable for most digital electronics systems.

Nvis 6561 has built-in clock source, logic high-low input facility and LEDs for visual indication of input-output logic states. Besides this, a +5V DC adaptor is also provided for power supply.

Features

- Stand alone system
- Easy illustration of all types of 3-bit and 4-bit shift registers
- LEDs for visual indication of input and output logic states
- SPDT switches for easy input logic selection
- Online product tutorial

Scope of Learning

- Study of 3-bit and 4-bit serial in serial out shift register (using D Flip-Flops)
- Study of 3-bit and 4-bit serial in parallel out shift register (using D Flip-Flops)
- Study of 3-bit and 4-bit parallel in serial out shift register (using D Flip-Flops)
- Study of 3-bit and 4-bit parallel in parallel out shift register (using D Flip-Flops)

Technical Specifications

DC Power Supply	: +5V
Logic levels	
+5 V	: High (Logic 1)
0V	: Low (Logic 0)
LED Indication	: LED will be ON for logic high or '1' state and will be OFF for logic low or '0' state
Dimensions (mm)	: 260 W x 355 D x 125 H