



Nvis 6565 SOP & POS Implementation Trainer is a compact, ready to use digital electronics experiment board. It is useful for students to get a practical insight into the implementation of different canonical forms. The various applications of a canonical forms are representing any Boolean function as a product of sum or sum of product. Nvis 6565 has built in clock source, logic high-low input facility and LEDs for visual indication of input-output states. Besides this, a +5V DC adaptor is also provided for power supply.

Features

- Stand alone system
- Easy illustration of different types of canonical forms
- LEDs for visual indication of input and output logic states
- SPDT switches for input logic selection
- Online product tutorial

Scope of Learning

- Study of boolean function in different canonical forms
- Study of Quine Mclusky method and realize its NOR-OR implementation

Technical Specifications

DC Power Supply	: +5V DC
Logic levels	
+5V	: High (Logic 1)
0V	: Low (Logic 0)
LED Indication	: LED will be ON for logic high or '1' state and will be OFF for logic low or '0' state
Dimensions (mm)	: W 260 x D 355 x H 125